Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application. Please amend the claims as follows:

Listing of Claims:

1. (Original) An active termination circuit for setting the input impedance of a plurality of input terminals to a predetermined value, the active termination circuit comprising:

a first controllable impedance device coupled between a first supply voltage and a respective one of the input terminals, the impedance of the first controllable impedance device being controlled by a first impedance control signal;

a second controllable impedance device coupled between a second supply voltage and a respective one of the input terminals, the impedance of the second controllable impedance device being controlled by a second impedance control signal;

a first control circuit coupled to provide the first impedance control signal to all of the first controllable impedance devices, the first control circuit comprising:

a third controllable impedance device coupled between a third supply voltage and a first feedback node, the impedance of the third controllable impedance device being controlled by the first impedance control signal;

a first predetermined resistance coupled between the first feedback node and a fourth supply voltage, the third controllable impedance device and the first predetermined resistance forming a voltage divider between the third and fourth supply voltages to produce a first feedback voltage at the first feedback node; and

a first comparator circuit comparing the first feedback voltage to a first reference voltage, the first comparator circuit causing the first impedance control signal to vary so that the first feedback voltage is substantially equal to the first reference voltage; and

a second control circuit coupled to provide the second impedance control signal to all of the second controllable impedance devices, the second control circuit comprising:

a second predetermined resistance coupled between a fifth supply voltage and a second feedback node,

a fourth controllable impedance device coupled between the second feedback node and a sixth supply voltage, the impedance of the fourth controllable impedance device being controlled by the second impedance control signal, the second predetermined resistance and the fourth controllable impedance device forming a voltage divider between the fifth and sixth supply voltages to produce a second feedback voltage at the second feedback node; and

a second comparator circuit comparing the second feedback voltage to a second reference voltage, the second comparator circuit causing the second impedance control signal to vary so that the second feedback voltage is substantially equal to the second reference voltage.

Claims 2-74 (Cancelled)